# Bosch Video Management System Bosch VMS, Mobile App





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# 1 Introduction

This document contains the Open Source Software licenses used in Bosch VMS and Mobile App.

The packages are either used completely or only some parts within a package are used. The applicable license is listed depending on whether the complete package is used or only parts of the package are used.

# 2 Overview

This chapter provides overviews on the used Open Source Licenses in Bosch VMS and Mobile App.

### 2.1 Bosch VMS OSS licenses

For general information regarding open source software in Bosch Security Systems please visit http://www.boschsecurity.com/oss

The following table lists the Open Source Software licenses used in Bosch VMS.

#### **Bosch VMS Open Source Licenses**

Used components sorted by name:

- A WTL DocView framework
- Antlr
- APNS-Sharp
- Azzeton
- Backload
- Bugslayer
- Command Line Arguments Parser
- crearmore
- CSubclassWnd
- DotNetZip
- Execution Time Based Heuristic Custom Task Scheduler
- Extended WPF Toolkit Community Edition
- FFmpeg
- g711
- Gladman Older Rijndael Implementation
- h2i-digitalimageprocessing
- Intel IPP
- Intel JPEG Library
- Intel Media SDK
- Javascript Date Time Picker
- jQuery
- jQuery idleTime plugin
- JQuery UI
- jQuery validation
- Json.NET
- legendcraft
- libvpx
- log4net
- mongodb-net
- NetAccess
- NLib
- NSTL
- Performance Counter Test App
- PS3BlueMote
- RegSvrEx
- Rhino.Mocks
- Spring Framework .NET
- supersocket

- ucmaclientbilling

- UPnP for C
- WebGrease
- WinMgr
- WTL
- XMLReader

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Used components sorted by license type:

Andreas Mueller License: - NSTL (2.5.119.0)

Apache License Version 2.0:

- crearmore
- Execution Time Based Heuristic Custom Task Scheduler
- h2i-digitalimageprocessing
- log4net
- mongodb-net
- Performance Counter Test App
- Spring Framework .NET
- supersocket
- UPnP for C
- WebGrease

BSD 2-clause License: - Javascript Date Time Picker

BSD 3-clause License:

- Antlr
- libvpx
- Rhino.Mocks

Code Project Open License 1.02:

- A WTL DocView framework
- RegSvrEx

Common Public License 1.0: - WTL

Gladman Older Rijndael License: - Gladman Older Rijndael Implementation

GNU Lesser General Public License, Version 2.1: - FFmpeg

Intel JPEG Library License: - Intel JPEG Library Intel Media SDK License:

- Intel Media SDK

Intel Software Development Products License:

- Intel IPP

Microsoft Public License:

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- Extended WPF Toolkit Community Edition
- NLib
- ucmaclientbilling

Microsoft Reciprocal License:

- Azzeton

Microsoft Systems Journal Sample Code License:

- Bugslayer
- CSubclassWnd
- WinMgr

MIT License:

- APNS-Sharp
- Backload
- Command Line Arguments Parser
- jQuery
- jQuery idleTime plugin (0.8.092209)
- JQuery UI
- jQuery validation
- Json.NET
- legendcraft
- NetAccess
- PS3BlueMote
- XMLReader

Sun License: - g711

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#### Mobile App Open Source Licenses

ID	Component	Copyright Holder	OS License
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07	XMLReader	https://github.com/amarcadet/XMLReader	MIT License

# **3** OSS licenses for Bosch VMS

### 3.1 A WTL DocView framework

Component: A WTL DocView framework URL: http://www.codeproject.com/Articles/11251/A-WTL-DocView-framework License Type: Code Project Open License 1.02

The Code Project Open License (CPOL) 1.02

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### 3.3 APNS-Sharp

Component: APNS-Sharp URL: https://github.com/Redth/APNS-Sharp License Type: MIT License

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Component: Azzeton URL: http://azzeton.codeplex.com License Type: Microsoft Reciprocal License

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### 3.5 Backload

Component: Backload URL: http://backloaddemo.codeplex.com/ License Type: MIT License

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### 3.6 Bugslayer

Component: Bugslayer URL: n/a License Type: Microsoft Systems Journal Sample Code License

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Component: Burning and Erasing CD/DVD/Blu-ray Media with C# and IMAPI2 URL: http://www.codeproject.com/Articles/24544/Burning-and-Erasing-CD-DVD-Blu-ray-Mediawith-C-an

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3.8

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Component: Command Line Arguments Parser URL: http://www.codeproject.com/Articles/3111/C-NET-Command-Line-Arguments-Parser License Type: MIT License

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### 3.12 Execution Time Based Heuristic Custom Task Scheduler

Component: Execution Time Based Heuristic Custom Task Scheduler URL: http://code.msdn.microsoft.com/Execution-Time-Based-dd4dbdb6 License Type: Apache License Version 2.0

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## 3.15 g711

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## **3.16 Gladman Older Rijndael Implementation**

Component: Gladman Older Rijndael Implementation URL: http://gladman.plushost.co.uk/oldsite/cryptography\_technology/rijndael License Type: Gladman Older Rijndael License

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# 3.17 h2i-digitalimageprocessing

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# 3.19 Intel JPEG Library

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## 3.20 Intel Media SDK

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## 3.21 Javascript Date Time Picker

Component: Javascript Date Time Picker URL: http://www.rainforestnet.com/datetimepicker/datetimepicker.htm License Type: BSD 2-clause License

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# 3.23 jQuery idleTime plugin

Component: jQuery idleTime plugin Version: 0.8.092209 URL: https://github.com/mikesherov/jquery-idletimer License Type: MIT License

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Component: JQuery UI URL: https://github.com/jquery/jquery-ui License Type: MIT License

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# 3.25 jQuery validation

Component: jQuery validation URL: https://github.com/jzaefferer/jquery-validation License Type: MIT License

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## 3.27 legendcraft

Component: legendcraft URL: https://github.com/LeChosenOne/LegendCraft/ License Type: MIT License

------LegendCraft

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#### 3.28 libvpx

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## 3.30 mongodb-net

Component: mongodb-net

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## 3.35 PS3BlueMote

Component: PS3BlueMote URL: https://github.com/Ben-Barron/PS3BluMote License Type: MIT License

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Component: RegSvrEx URL: http://www.codeproject.com/Articles/3505/RegSvrEx-An-Enchanced-COM-Server-Registration-Util License Type: Code Project Open License 1.02

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# 3.37 Rhino.Mocks

Component: Rhino.Mocks URL: http://hibernatingrhinos.com/oss/rhino-mocks License Type: BSD 3-clause License

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# 3.38 SharpDX

Component: SharpDX URL: https://github.com/sharpdx/SharpDX/ License Type: MIT License

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# 3.39 Spring Framework .NET

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## 3.43 WebGrease

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### HttpNamespaceManager

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cryptool http://github.com/ollej/cryptool/

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A "covered work" means either the unmodified Program or a work based on the Program.

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The "source code" for a work means the preferred form of the work for making modifications to it. "Object code" means any non-source form of a work.

A "Standard Interface" means an interface that either is an official standard defined by a recognized standards body, or, in the case of interfaces specified for a particular programming language, one that is widely used among developers working in that language.

The "System Libraries" of an executable work include anything, other than the work as a whole, that (a) is included in the normal form of packaging a Major Component, but which is not part of that Major Component, and (b) serves only to enable use of the work with that Major Component, or to implement a Standard Interface for which an implementation is available to the public in source code form. A "Major Component", in this context, means a major essential component (kernel, window system, and so on) of the specific operating system (if any) on which the executable work runs, or a compiler used to produce the work, or an object code interpreter used to run it.

The "Corresponding Source" for a work in object code form means all the source code needed to generate, install, and (for an executable work) run the object code and to modify the work, including scripts to control those activities. However, it does not include the work's System Libraries, or general-purpose tools or generally available free programs which are used unmodified in performing those activities but which are not part of the work. For example, Corresponding Source includes interface definition files associated with source files for the work, and the source code for shared libraries and dynamically linked subprograms that the work is specifically designed to require, such as by intimate data communication or control flow between those subprograms and other parts of the work.

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c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This

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hash.c:

/\*

\* hash.c: chained hash tables

\* Reference: Your favorite introductory book on algorithms

\*

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\* Author: breese@users.sourceforge.net \*/

list.c:

/\*

\* list.c: lists handling implementation

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component: RFC 6234 Secure Hash Algorithm url: http://tools.ietf.org/html/rfc6234 type: RFC

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component: RSA Data Security-MD5 Message url: http://tools.ietf.org/html/rfc1321 type: RSA Data Security

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component: zlib url: http://www.zlib.net/ type: zlib

/\* zlib.h -- interface of the 'zlib' general purpose compression library version 1.2.8, April 28th, 2013

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- RFC 6234 Secure Hash Algorithm

RSA Data Security: - RSA Data Security-MD5 Message

zlib:

- NSIS installer icons
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component: iniparser url: https://github.com/ndevilla/iniparser/ type: MIT

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component: Intel Media SDK for Clients version: 2014 (5.0.0000337.78585) url: http://software.intel.com/en-us/vcsource/tools/media-sdk type: BSD-3-Clause

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component: jQuery JavaScript Library version: 1.7.1 / 1.8.3 / 1.10.2 url: http://jquery.com/ type: MIT

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component: jquery-handsontable url: http://github.com/warpech/jquery-handsontable/ type: MIT

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component: jquery-mobile version: 1.3.2 url: http://github.com/jquery/jquery-mobile/ type: MIT

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component: jquery-ui-touch-punch url: http://github.com/furf/jquery-ui-touch-punch/ type: MIT

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component: jquery.maskedinput url: http://github.com/digitalBush/jquery.maskedinput/ type: MIT

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component: jQuery.selectBoxIt.js url: http://github.com/gfranko/jQuery.selectBoxIt.js/ type: MIT

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component: jqueryjs version: 1.3.2 url: http://code.google.com/p/jqueryjs/ type: MIT

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component: jqueryui.com version: 1.10.0-2 url: http://github.com/jquery/jqueryui.com/ type: MIT

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component: jScrollPane version: 2.0.14 and other url: http://github.com/vitch/jScrollPane/ type: MIT

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component: jStorage version: 0.4.0 url: https://github.com/andris9/jStorage/ type: MIT

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component: jsTree url: http://sourceforge.net/projects/jstree/ type: MIT

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component: knockout-deferred-updates version: 3.1.0 url: https://github.com/mbest/knockout-deferred-updates/ type: MIT

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component: Lazy Load Plugin for jQuery url: http://www.appelsiini.net/projects/lazyload type: MIT

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component: libusb-win32 version: 1.2.6.0 url: https://sourceforge.net/projects/libusb-win32/ type: LGPLv3

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\* hash.c: chained hash tables

\* Reference: Your favorite introductory book on algorithms

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list.c:

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\* list.c: lists handling implementation

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Based on GIFENCOD by David Rowley <mgardi@watdscu.waterloo.edu>. A Lempel-Zim compression based on "compress".

Modified by Marcel Wijkstra <wijkstra@fwi.uva.nl>

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One file was modified (include/cistpl.h), file content ends after "#endif /\* CISTPL\_H \*/":

### /\*

\* cistpl.h 1.35 2001/08/24 12:16:12

\*

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/\*

\* Changes compared to the original from pcmcia-cs, version 3.2.8:

\* - 2014-02-23, Joachim Meichle:

\* - Rename inclusion guard from "\_LINUX\_CISTPL\_H" to "CISTPL\_H"

\* - Replace zero size arrays with arrays of size one.

\* Zero sized arrays are an non-standard and a compiler specific

\* extension of GCC.

\* - Add "typedef u\_char cisdata\_t;" to make file "more" self-containing \*/

#ifndef CISTPL\_H #define CISTPL H

#define CISTPL NULL 0x00 #define CISTPL DEVICE 0x01 #define CISTPL LONGLINK CB 0x02 #define CISTPL\_INDIRECT 0x03 #define CISTPL CONFIG CB 0x04 #define CISTPL CFTABLE ENTRY CB 0x05 #define CISTPL LONGLINK MFC 0x06 #define CISTPL BAR 0x07 #define CISTPL PWR MGMNT 0x08 #define CISTPL EXTDEVICE 0x09 #define CISTPL\_CHECKSUM 0x10 #define CISTPL LONGLINK A 0x11 #define CISTPL LONGLINK C 0x12 #define CISTPL LINKTARGET 0x13 #define CISTPL\_NO\_LINK 0x14 #define CISTPL\_VERS\_1 0x15 #define CISTPL ALTSTR 0x16 #define CISTPL\_DEVICE\_A 0x17 #define CISTPL JEDEC C 0x18 #define CISTPL\_JEDEC\_A 0x19 #define CISTPL CONFIG 0x1a #define CISTPL\_CFTABLE\_ENTRY 0x1b #define CISTPL\_DEVICE\_OC 0x1c #define CISTPL DEVICE OA 0x1d #define CISTPL\_DEVICE\_GEO 0x1e #define CISTPL DEVICE GEO A 0x1f #define CISTPL MANFID 0x20 #define CISTPL FUNCID 0x21 #define CISTPL FUNCE 0x22 #define CISTPL\_SWIL 0x23 #define CISTPL END 0xff /\* Layer 2 tuples \*/ #define CISTPL\_VERS\_2 0x40 #define CISTPL FORMAT 0x41 #define CISTPL GEOMETRY 0x42 #define CISTPL BYTEORDER 0x43 #define CISTPL\_DATE 0x44 #define CISTPL BATTERY 0x45 #define CISTPL\_FORMAT\_A 0x47 /\* Layer 3 tuples \*/ #define CISTPL ORG 0x46 #define CISTPL\_SPCL 0x90

typedef u\_char cisdata\_t;

typedef struct cistpl\_longlink\_t {
 u\_int addr;
} cistpl\_longlink\_t;

typedef struct cistpl\_checksum\_t {
 u\_short addr;
 u\_short len;
 u\_char sum;
} cistpl\_checksum\_t;

#define CISTPL\_MAX\_FUNCTIONS 8
#define CISTPL\_MFC\_ATTR 0x00
#define CISTPL\_MFC\_COMMON 0x01

typedef struct cistpl\_longlink\_mfc\_t {
 u\_char nfn;
 struct {
 u\_char space;
 u\_int addr;
 } fn[CISTPL\_MAX\_FUNCTIONS];
 } cistpl\_longlink\_mfc\_t;

#define CISTPL\_MAX\_ALTSTR\_STRINGS 4

typedef struct cistpl\_altstr\_t {
 u\_char ns;
 u\_char ofs[CISTPL\_MAX\_ALTSTR\_STRINGS];
 char str[254];
} cistpl\_altstr\_t;

#define CISTPL\_DTYPE\_NULL 0x00
#define CISTPL\_DTYPE\_ROM 0x01
#define CISTPL\_DTYPE\_OTPROM 0x02
#define CISTPL\_DTYPE\_EPROM 0x03
#define CISTPL\_DTYPE\_FLASH 0x05
#define CISTPL\_DTYPE\_SRAM 0x06
#define CISTPL\_DTYPE\_DRAM 0x07
#define CISTPL\_DTYPE\_FUNCSPEC 0x0d
#define CISTPL\_DTYPE\_EXTEND 0x0e

#define CISTPL\_MAX\_DEVICES 4

typedef struct cistpl\_device\_t {
 u\_char ndev;
 struct {
 u\_char type;
 u\_char wp;
 u\_int speed;

u int size; } dev[CISTPL MAX DEVICES]; } cistpl device t; #define CISTPL\_DEVICE\_MWAIT 0x01 #define CISTPL DEVICE 3VCC 0x02 typedef struct cistpl device o t { u char flags; cistpl\_device\_t device; } cistpl\_device\_o\_t; #define CISTPL\_VERS\_1\_MAX\_PROD\_STRINGS 4 typedef struct cistpl vers 1 t { u char major; u\_char minor; u char ns; u\_char ofs[CISTPL\_VERS\_1\_MAX\_PROD\_STRINGS]; char str[254]; } cistpl\_vers\_1\_t; typedef struct cistpl\_jedec\_t { u\_char nid; struct { u\_char mfr; u char info; } id[CISTPL\_MAX\_DEVICES]; } cistpl\_jedec\_t; typedef struct cistpl\_manfid\_t { u short manf; u\_short card; } cistpl\_manfid\_t; #define CISTPL\_FUNCID\_MULTI 0x00 #define CISTPL\_FUNCID\_MEMORY 0x01 #define CISTPL\_FUNCID\_SERIAL 0x02 #define CISTPL\_FUNCID\_PARALLEL 0x03 #define CISTPL\_FUNCID\_FIXED 0x04 #define CISTPL\_FUNCID\_VIDEO 0x05 #define CISTPL\_FUNCID\_NETWORK 0x06 #define CISTPL\_FUNCID\_AIMS 0x07 #define CISTPL\_FUNCID\_SCSI 0x08

#define CISTPL\_SYSINIT\_POST 0x01 #define CISTPL\_SYSINIT\_ROM 0x02

typedef struct cistpl\_funcid\_t {
u\_char func;

u\_char sysinit; } cistpl\_funcid\_t;

typedef struct cistpl\_funce\_t {
 u\_char type;
 u\_char data[1];
} cistpl funce t;

/\*\_\_\_\_\_

Modem Function Extension Tuples

-----\*/

#define CISTPL\_FUNCE\_SERIAL\_IF 0x00
#define CISTPL\_FUNCE\_SERIAL\_CAP 0x01
#define CISTPL\_FUNCE\_SERIAL\_SERV\_DATA 0x02
#define CISTPL\_FUNCE\_SERIAL\_SERV\_VOICE 0x03
#define CISTPL\_FUNCE\_SERIAL\_CAP\_DATA 0x05
#define CISTPL\_FUNCE\_SERIAL\_CAP\_FAX 0x06
#define CISTPL\_FUNCE\_SERIAL\_CAP\_VOICE 0x07
#define CISTPL\_FUNCE\_SERIAL\_IF\_DATA 0x08
#define CISTPL\_FUNCE\_SERIAL\_IF\_FAX 0x09
#define CISTPL\_FUNCE\_SERIAL\_IF\_VOICE 0x0a

/\* UART identification \*/
#define CISTPL\_SERIAL\_UART\_8250 0x00
#define CISTPL\_SERIAL\_UART\_16450 0x01
#define CISTPL\_SERIAL\_UART\_16550 0x02
#define CISTPL\_SERIAL\_UART\_8251 0x03
#define CISTPL\_SERIAL\_UART\_8530 0x04
#define CISTPL\_SERIAL\_UART\_85230 0x05

/\* UART capabilities \*/

#define CISTPL\_SERIAL\_UART\_SPACE 0x01 #define CISTPL\_SERIAL\_UART\_MARK 0x02 #define CISTPL\_SERIAL\_UART\_ODD 0x04 #define CISTPL\_SERIAL\_UART\_EVEN 0x08 #define CISTPL\_SERIAL\_UART\_5BIT 0x01 #define CISTPL\_SERIAL\_UART\_6BIT 0x02 #define CISTPL\_SERIAL\_UART\_7BIT 0x04 #define CISTPL\_SERIAL\_UART\_8BIT 0x08 #define CISTPL\_SERIAL\_UART\_1STOP 0x10 #define CISTPL\_SERIAL\_UART\_1STOP 0x20 #define CISTPL\_SERIAL\_UART\_2STOP 0x40

typedef struct cistpl\_serial\_t {
u\_char uart\_type;
u\_char uart\_cap\_0;

u\_char uart\_cap\_1; } cistpl\_serial\_t;

typedef struct cistpl\_modem\_cap\_t {
 u\_char flow;
 u\_char cmd\_buf;
 u\_char rcv\_buf\_0, rcv\_buf\_1, rcv\_buf\_2;
 u\_char xmit\_buf\_0, xmit\_buf\_1, xmit\_buf\_2;
} cistpl\_modem\_cap\_t;

#define CISTPL\_SERIAL\_MOD\_103 0x01 #define CISTPL\_SERIAL\_MOD\_V21 0x02 #define CISTPL\_SERIAL\_MOD\_V23 0x04 #define CISTPL\_SERIAL\_MOD\_V22 0x08 #define CISTPL\_SERIAL\_MOD\_V22 0x00 #define CISTPL\_SERIAL\_MOD\_V22BIS 0x20 #define CISTPL\_SERIAL\_MOD\_V26 0x40 #define CISTPL\_SERIAL\_MOD\_V26BIS 0x80 #define CISTPL\_SERIAL\_MOD\_V27BIS 0x01 #define CISTPL\_SERIAL\_MOD\_V27BIS 0x01 #define CISTPL\_SERIAL\_MOD\_V29 0x02 #define CISTPL\_SERIAL\_MOD\_V32 0x04 #define CISTPL\_SERIAL\_MOD\_V32BIS 0x08 #define CISTPL\_SERIAL\_MOD\_V34 0x10

#define CISTPL\_SERIAL\_ERR\_MNP2\_4 0x01
#define CISTPL\_SERIAL\_ERR\_V42\_LAPM 0x02

#define CISTPL\_SERIAL\_CMPR\_V42BIS 0x01
#define CISTPL\_SERIAL\_CMPR\_MNP5 0x02

#define CISTPL\_SERIAL\_CMD\_AT1 0x01
#define CISTPL\_SERIAL\_CMD\_AT2 0x02
#define CISTPL\_SERIAL\_CMD\_AT3 0x04
#define CISTPL\_SERIAL\_CMD\_MNP\_AT 0x08
#define CISTPL\_SERIAL\_CMD\_V25BIS 0x10
#define CISTPL\_SERIAL\_CMD\_V25A 0x20
#define CISTPL\_SERIAL\_CMD\_DMCL 0x40

typedef struct cistpl\_data\_serv\_t {
 u\_char max\_data\_0;
 u\_char max\_data\_1;
 u\_char modulation\_0;
 u\_char modulation\_1;
 u\_char error\_control;
 u\_char compression;
 u\_char cmd\_protocol;
 u\_char encrypt;
 u\_char encrypt;
 u\_char misc\_features;
 u\_char ccitt\_code[1];

} cistpl\_data\_serv\_t;

typedef struct cistpl\_fax\_serv\_t {
 u\_char max\_data\_0;
 u\_char max\_data\_1;
 u\_char modulation;
 u\_char encrypt;
 u\_char features\_0;
 u\_char features\_1;
 u\_char ccitt\_code[1];
 } cistpl\_fax\_serv\_t;

typedef struct cistpl\_voice\_serv\_t {
 u\_char max\_data\_0;
 u\_char max\_data\_1;
 } cistpl\_voice\_serv\_t;

LAN Function Extension Tuples

\*/

#define CISTPL\_FUNCE\_LAN\_TECH 0x01
#define CISTPL\_FUNCE\_LAN\_SPEED 0x02
#define CISTPL\_FUNCE\_LAN\_MEDIA 0x03
#define CISTPL\_FUNCE\_LAN\_NODE\_ID 0x04
#define CISTPL\_FUNCE\_LAN\_CONNECTOR 0x05

/\* LAN technologies \*/
#define CISTPL\_LAN\_TECH\_ARCNET 0x01
#define CISTPL\_LAN\_TECH\_ETHERNET 0x02
#define CISTPL\_LAN\_TECH\_TOKENRING 0x03
#define CISTPL\_LAN\_TECH\_LOCALTALK 0x04
#define CISTPL\_LAN\_TECH\_FDDI 0x05
#define CISTPL\_LAN\_TECH\_ATM 0x06
#define CISTPL\_LAN\_TECH\_WIRELESS 0x07

typedef struct cistpl\_lan\_tech\_t {
 u\_char tech;
} cistpl\_lan\_tech\_t;

typedef struct cistpl\_lan\_speed\_t {
 u\_int speed;
} cistpl\_lan\_speed\_t;

/\* LAN media definitions \*/
#define CISTPL\_LAN\_MEDIA\_UTP 0x01
#define CISTPL\_LAN\_MEDIA\_STP 0x02
#define CISTPL\_LAN\_MEDIA\_THIN\_COAX 0x03

<pre>#define CISTPL_LAN_MEDIA_THICK_COAX 0x04 #define CISTPL_LAN_MEDIA_FIBER 0x05 #define CISTPL_LAN_MEDIA_900MHZ 0x06 #define CISTPL_LAN_MEDIA_2GHZ 0x07 #define CISTPL_LAN_MEDIA_5GHZ 0x08 #define CISTPL_LAN_MEDIA_DIFF_IR 0x09 #define CISTPL_LAN_MEDIA_PTP_IR 0x0a</pre>
typedef struct cistpl_lan_media_t { u_char media; } cistpl_lan_media_t;
typedef struct cistpl_lan_node_id_t { u_char nb; u_char id[16]; } cistpl_lan_node_id_t;
typedef struct cistpl_lan_connector_t { u_char code; } cistpl_lan_connector_t;
/*====================================
#define CISTPL_IDE_INTERFACE 0x01
*/
<pre>#define CISTPL_IDE_INTERFACE 0x01 typedef struct cistpl_ide_interface_t { u_char interface;</pre>

typedef struct cistpl\_ide\_feature\_t {
u\_char feature1;
u\_char feature2;

} cistpl\_ide\_feature\_t;

#define CISTPL\_FUNCE\_IDE\_IFACE 0x01
#define CISTPL\_FUNCE\_IDE\_MASTER 0x02
#define CISTPL\_FUNCE\_IDE\_SLAVE 0x03

/\*\_\_\_\_\_

**Configuration Table Entries** 

-----\*/

#define CISTPL\_BAR\_SPACE 0x07
#define CISTPL\_BAR\_SPACE\_IO 0x10
#define CISTPL\_BAR\_PREFETCH 0x20
#define CISTPL\_BAR\_CACHEABLE 0x40
#define CISTPL\_BAR\_1MEG\_MAP 0x80

typedef struct cistpl\_bar\_t {
 u\_char attr;
 u\_int size;
} cistpl\_bar\_t;

typedef struct cistpl\_config\_t {
 u\_char last\_idx;
 u\_int base;
 u\_int rmask[4];
 u\_char subtuples;
 } cistpl\_config\_t;

/\* These are bits in the 'present' field, and indices in 'param' \*/
#define CISTPL\_POWER\_VNOM 0
#define CISTPL\_POWER\_VMIN 1
#define CISTPL\_POWER\_VMAX 2
#define CISTPL\_POWER\_ISTATIC 3
#define CISTPL\_POWER\_IAVG 4
#define CISTPL\_POWER\_IPEAK 5
#define CISTPL\_POWER\_IDOWN 6

#define CISTPL\_POWER\_HIGHZ\_OK 0x01
#define CISTPL\_POWER\_HIGHZ\_REQ 0x02

typedef struct cistpl\_power\_t {
u\_char present;
u\_char flags;
u\_int param[7];
} cistpl\_power\_t;

typedef struct cistpl\_timing\_t {
u\_int wait, waitscale;

u\_int ready, rdyscale; u\_int reserved, rsvscale; } cistpl\_timing\_t;

#define CISTPL\_IO\_LINES\_MASK 0x1f #define CISTPL\_IO\_8BIT 0x20 #define CISTPL\_IO\_16BIT 0x40 #define CISTPL\_IO\_RANGE 0x80

#define CISTPL\_IO\_MAX\_WIN 16

typedef struct cistpl\_io\_t {
 u\_char flags;
 u\_char nwin;
 struct {
 u\_int base;
 u\_int len;
 } win[CISTPL\_IO\_MAX\_WIN];
 } cistpl\_io\_t;

typedef struct cistpl\_irq\_t {
u\_int IRQInfo1;
u\_int IRQInfo2;
} cistpl\_irq\_t;

#define CISTPL\_MEM\_MAX\_WIN 8

typedef struct cistpl\_mem\_t {
 u\_char flags;
 u\_char nwin;
 struct {
 u\_int len;
 u\_int card\_addr;
 u\_int host\_addr;
 } win[CISTPL\_MEM\_MAX\_WIN];
 } cistpl\_mem\_t;

#define CISTPL\_CFTABLE\_DEFAULT 0x0001
#define CISTPL\_CFTABLE\_BVDS 0x0002
#define CISTPL\_CFTABLE\_WP 0x0004
#define CISTPL\_CFTABLE\_RDYBSY 0x0008
#define CISTPL\_CFTABLE\_MWAIT 0x0010
#define CISTPL\_CFTABLE\_AUDIO 0x0800
#define CISTPL\_CFTABLE\_READONLY 0x1000
#define CISTPL\_CFTABLE\_PWRDOWN 0x2000

typedef struct cistpl\_cftable\_entry\_t {
u\_char index;
u\_short flags;
u\_char interface;

cistpl power t vcc, vpp1, vpp2; cistpl\_timing\_t timing; cistpl io t io; cistpl irq t irq; cistpl\_mem\_t mem; u char subtuples; } cistpl\_cftable\_entry\_t; #define CISTPL CFTABLE MASTER 0x000100 #define CISTPL\_CFTABLE\_INVALIDATE 0x000200 #define CISTPL CFTABLE VGA PALETTE 0x000400 #define CISTPL CFTABLE PARITY 0x000800 #define CISTPL CFTABLE WAIT 0x001000 #define CISTPL CFTABLE SERR 0x002000 #define CISTPL CFTABLE FAST BACK 0x004000 #define CISTPL CFTABLE BINARY AUDIO 0x010000 #define CISTPL\_CFTABLE\_PWM\_AUDIO 0x020000

typedef struct cistpl\_cftable\_entry\_cb\_t {
 u\_char index;
 u\_int flags;
 cistpl\_power\_t vcc, vpp1, vpp2;
 u\_char io;
 cistpl\_irq\_t irq;
 u\_char mem;
 u\_char subtuples;
 } cistpl\_cftable\_entry\_cb\_t;

typedef struct cistpl\_device\_geo\_t {
 u\_char ngeo;
 struct {
 u\_char buswidth;
 u\_int erase\_block;
 u\_int read\_block;
 u\_int write\_block;
 u\_int partition;
 u\_int interleave;
 } geo[CISTPL\_MAX\_DEVICES];
 } cistpl\_device\_geo\_t;

typedef struct cistpl\_vers\_2\_t {
 u\_char vers;
 u\_char comply;
 u\_short dindex;
 u\_char vspec8, vspec9;
 u\_char nhdr;
 u\_char vendor, info;
 char str[244];
} cistpl\_vers\_2\_t;

typedef struct cistpl\_org\_t {
 u\_char data\_org;
 char desc[30];
} cistpl\_org\_t;

#define CISTPL\_ORG\_FS 0x00 #define CISTPL\_ORG\_APPSPEC 0x01 #define CISTPL\_ORG\_XIP 0x02

typedef struct cistpl\_format\_t {
 u\_char type;
 u\_char edc;
 u\_int offset;
 u\_int length;
} cistpl format t;

#define CISTPL\_FORMAT\_DISK 0x00
#define CISTPL\_FORMAT\_MEM 0x01

#define CISTPL\_EDC\_NONE 0x00
#define CISTPL\_EDC\_CKSUM 0x01
#define CISTPL\_EDC\_CRC 0x02
#define CISTPL\_EDC\_PCC 0x03

typedef union cisparse t { cistpl\_device\_t device; cistpl checksum t checksum; cistpl\_longlink\_t longlink; cistpl\_longlink\_mfc\_t longlink\_mfc; cistpl\_vers\_1\_t version\_1; cistpl\_altstr\_t altstr; cistpl jedec t jedec; cistpl\_manfid\_t manfid; cistpl funcid t funcid; cistpl\_funce\_t funce; cistpl\_bar\_t bar; cistpl\_config\_t config; cistpl\_cftable\_entry\_t cftable\_entry; cistpl\_cftable\_entry\_cb\_t cftable\_entry\_cb; cistpl\_device\_geo\_t device\_geo; cistpl\_vers\_2\_t vers\_2; cistpl\_org\_t org; cistpl\_format\_t format; } cisparse\_t;

```
typedef struct tuple_t {
  u_int Attributes;
  cisdata_t DesiredTuple;
  u_int Flags; /* internal use */
  u_int LinkOffset; /* internal use */
```

u\_int CISOffset; /\* internal use \*/
cisdata\_t TupleCode;
cisdata\_t TupleLink;
cisdata\_t TupleOffset;
cisdata\_t TupleDataMax;
cisdata\_t TupleDataLen;
cisdata\_t \*TupleData;
} tuple t;

/\* Special cisdata\_t value \*/ #define RETURN\_FIRST\_TUPLE 0xff

/\* Attributes for tuple calls \*/ #define TUPLE\_RETURN\_LINK 0x01 #define TUPLE\_RETURN\_COMMON 0x02

/\* For ValidateCIS \*/
typedef struct cisinfo\_t {
 u\_int Chains;
} cisinfo\_t;

#define CISTPL\_MAX\_CIS\_SIZE 0x200

/\* For ReplaceCIS \*/
typedef struct cisdump\_t {
 u\_int Length;
 cisdata\_t Data[CISTPL\_MAX\_CIS\_SIZE];
} cisdump\_t;

#endif /\* CISTPL\_H \*/

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component: PIE version: 20120512 url: http://github.com/lojjic/PIE/ type: Apache-2.0

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component: PolyK library url: http://polyk.ivank.net/ type: MIT

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component: QDBM url: http://fallabs.com/qdbm/ type: LGPLv2

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component: quick-and-dirty XML parser url: http://www.javaworld.com/javatips/jw-javatip128.html type: MIT

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component: Quickfunctor url: http://sourceforge.net/projects/quickfunctor/ type: MIT

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component: rainforest Date Time Picker url: http://rainforestnet.com/datetimepicker type: BSD-2-Clause

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component: Raphael JavaScript Library url: http://raphaeljs.com/ type: MIT

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component: RapidXml

version: 1.13 url: http://sourceforge.net/projects/rapidxml/ type: MIT

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component: remdebug url: http://github.com/LuaDist/remdebug/ type: MIT

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component: RFC 6234 Secure Hash Algorithm url: http://tools.ietf.org/html/rfc6234 type: RFC

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component: RSA Data Security-MD5 Message url: http://tools.ietf.org/html/rfc1321 type: RSA Data Security

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component: Struct library for LUA url: http://www.inf.puc-rio.br/~roberto/struct/ type: MIT

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component: timeentry url: http://github.com/kbwood/timeentry/ type: MIT

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component: Tiny Scrollbar jQuery plugin version: 1.81 url: http://baijs.nl/tinyscrollbar/ type: MIT

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component: Tooltip jQuery plugin version: 1.3 url: http://bassistance.de/jquery-plugins/jquery-plugin-tooltip/ type: MIT

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component: WinPcap - The Windows Packet Capture Library version: 4.0 url: http://www.winpcap.org/default.htm type: BSD-3-Clause

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/\* zlib.h -- interface of the 'zlib' general purpose compression library version 1.2.8, April 28th, 2013

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component: jQuery JavaScript Library version: 1.7.1 / 1.8.3 / 1.10.2 url: http://jquery.com/ type: MIT

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component: jQuery UI from code.google.com version: 1.8 url: http://code.google.com/p/jquery-ui/ type: MIT

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component: jquery-handsontable url: http://github.com/warpech/jquery-handsontable/ type: MIT

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component: jquery-mobile version: 1.3.2 url: http://github.com/jquery/jquery-mobile/ type: MIT

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component: jquery-treeview url: http://github.com/jzaefferer/jquery-treeview/ type: MIT

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component: jquery-ui-touch-punch url: http://github.com/furf/jquery-ui-touch-punch/ type: MIT

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component: jquery.maskedinput
url: http://github.com/digitalBush/jquery.maskedinput/
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component: jqueryjs version: 1.3.2 url: http://code.google.com/p/jqueryjs/ type: MIT

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component: jqueryui.com version: 1.10.0-2 url: http://github.com/jquery/jqueryui.com/ type: MIT

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component: jScrollPane version: 2.0.14 and other url: http://github.com/vitch/jScrollPane/ type: MIT

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component: jsTree

url: http://sourceforge.net/projects/jstree/ type: MIT

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component: Lazy Load Plugin for jQuery url: http://www.appelsiini.net/projects/lazyload type: MIT

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Code drawn from ppmtogif.c, from the pbmplus package

Based on GIFENCOD by David Rowley <mgardi@watdscu.waterloo.edu>. A Lempel-Zim compression based on "compress".

Modified by Marcel Wijkstra <wijkstra@fwi.uva.nl>

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Heavily modified by Mouse, 1998-02-12. Remove LZW compression. Added miGIF run length compression.

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component: pcmcia-cs version: 3.2.8 url: http://github.com/lumag/pcmcia-cs/ type: MPL 1.1

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One file was modified (include/cistpl.h), file content ends after "#endif /\* CISTPL H \*/":

/\*

\* cistpl.h 1.35 2001/08/24 12:16:12

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#### /\*

- \* Changes compared to the original from pcmcia-cs, version 3.2.8:
- \* 2014-02-23, Joachim Meichle:
- \* Rename inclusion guard from "\_LINUX\_CISTPL\_H" to "CISTPL\_H"
- \* Replace zero size arrays with arrays of size one.
- \* Zero sized arrays are an non-standard and a compiler specific
- \* extension of GCC.
- \* Add "typedef u\_char cisdata\_t;" to make file "more" self-containing \*/

#ifndef CISTPL\_H #define CISTPL\_H

#define CISTPL\_NULL 0x00
#define CISTPL\_DEVICE 0x01
#define CISTPL\_LONGLINK\_CB 0x02
#define CISTPL\_INDIRECT 0x03
#define CISTPL\_CONFIG\_CB 0x04
#define CISTPL\_CFTABLE\_ENTRY\_CB 0x05
#define CISTPL\_LONGLINK\_MFC 0x06
#define CISTPL\_BAR 0x07
#define CISTPL\_PWR\_MGMNT 0x08
#define CISTPL\_EXTDEVICE 0x09
#define CISTPL\_CHECKSUM 0x10
#define CISTPL\_LONGLINK\_A 0x11
#define CISTPL\_LONGLINK C 0x12

#define CISTPL\_LINKTARGET 0x13 #define CISTPL NO LINK 0x14 #define CISTPL VERS 1 0x15 #define CISTPL ALTSTR 0x16 #define CISTPL DEVICE A 0x17 #define CISTPL JEDEC C 0x18 #define CISTPL JEDEC A 0x19 #define CISTPL CONFIG 0x1a #define CISTPL CFTABLE ENTRY 0x1b #define CISTPL\_DEVICE\_OC 0x1c #define CISTPL DEVICE OA 0x1d #define CISTPL DEVICE GEO 0x1e #define CISTPL DEVICE GEO A 0x1f #define CISTPL MANFID 0x20 #define CISTPL FUNCID 0x21 #define CISTPL FUNCE 0x22 #define CISTPL\_SWIL 0x23 #define CISTPL END 0xff /\* Layer 2 tuples \*/ #define CISTPL VERS 2 0x40 #define CISTPL FORMAT 0x41 #define CISTPL\_GEOMETRY 0x42 #define CISTPL BYTEORDER 0x43 #define CISTPL\_DATE 0x44 #define CISTPL BATTERY 0x45 #define CISTPL FORMAT A 0x47 /\* Layer 3 tuples \*/ #define CISTPL ORG 0x46 #define CISTPL\_SPCL 0x90 typedef u\_char cisdata\_t;

typedef struct cistpl\_longlink\_t {
 u\_int addr;
} cistpl\_longlink\_t;

typedef struct cistpl\_checksum\_t {
 u\_short addr;
 u\_short len;
 u\_char sum;
} cistpl\_checksum\_t;

#define CISTPL\_MAX\_FUNCTIONS 8
#define CISTPL\_MFC\_ATTR 0x00
#define CISTPL\_MFC\_COMMON 0x01

typedef struct cistpl\_longlink\_mfc\_t {
u\_char nfn;
struct {
u\_char space;

u\_int addr; } fn[CISTPL\_MAX\_FUNCTIONS]; } cistpl\_longlink\_mfc\_t;

#define CISTPL\_MAX\_ALTSTR\_STRINGS 4

typedef struct cistpl\_altstr\_t {
 u\_char ns;
 u\_char ofs[CISTPL\_MAX\_ALTSTR\_STRINGS];
 char str[254];
} cistpl\_altstr\_t;

#define CISTPL\_DTYPE\_NULL 0x00
#define CISTPL\_DTYPE\_ROM 0x01
#define CISTPL\_DTYPE\_OTPROM 0x02
#define CISTPL\_DTYPE\_EPROM 0x03
#define CISTPL\_DTYPE\_EEPROM 0x04
#define CISTPL\_DTYPE\_FLASH 0x05
#define CISTPL\_DTYPE\_SRAM 0x06
#define CISTPL\_DTYPE\_DRAM 0x07
#define CISTPL\_DTYPE\_FUNCSPEC 0x0d
#define CISTPL\_DTYPE\_EXTEND 0x0e

#define CISTPL\_MAX\_DEVICES 4

typedef struct cistpl\_device\_t {
 u\_char ndev;
 struct {
 u\_char type;
 u\_char wp;
 u\_int speed;
 u\_int size;
 } dev[CISTPL\_MAX\_DEVICES];
 } cistpl\_device\_t;

#define CISTPL\_DEVICE\_MWAIT 0x01
#define CISTPL\_DEVICE\_3VCC 0x02

typedef struct cistpl\_device\_o\_t {
 u\_char flags;
 cistpl\_device\_t device;
} cistpl\_device\_o\_t;

#define CISTPL\_VERS\_1\_MAX\_PROD\_STRINGS 4

typedef struct cistpl\_vers\_1\_t {
 u\_char major;
 u\_char minor;
 u\_char ns;
 u\_char ofs[CISTPL\_VERS\_1\_MAX\_PROD\_STRINGS];

char str[254]; } cistpl\_vers\_1\_t; typedef struct cistpl\_jedec\_t { u char nid; struct { u char mfr; u char info; } id[CISTPL MAX DEVICES]; } cistpl\_jedec\_t; typedef struct cistpl\_manfid\_t { u short manf; u short card; } cistpl manfid t; #define CISTPL\_FUNCID\_MULTI 0x00 #define CISTPL FUNCID MEMORY 0x01 #define CISTPL FUNCID SERIAL 0x02 #define CISTPL FUNCID PARALLEL 0x03 #define CISTPL\_FUNCID\_FIXED 0x04 #define CISTPL\_FUNCID\_VIDEO 0x05 #define CISTPL FUNCID NETWORK 0x06 #define CISTPL\_FUNCID\_AIMS 0x07 #define CISTPL\_FUNCID\_SCSI 0x08 #define CISTPL SYSINIT POST 0x01 #define CISTPL\_SYSINIT\_ROM 0x02 typedef struct cistpl\_funcid\_t { u\_char func; u char sysinit; } cistpl\_funcid\_t; typedef struct cistpl funce t { u\_char type; u\_char data[1]; } cistpl\_funce\_t; Modem Function Extension Tuples -----\*/ #define CISTPL\_FUNCE\_SERIAL\_IF 0x00 #define CISTPL\_FUNCE\_SERIAL\_CAP 0x01 #define CISTPL\_FUNCE\_SERIAL\_SERV\_DATA 0x02 #define CISTPL\_FUNCE\_SERIAL\_SERV\_FAX 0x03 #define CISTPL\_FUNCE\_SERIAL\_SERV\_VOICE 0x04

#define CISTPL\_FUNCE\_SERIAL\_CAP\_DATA 0x05
#define CISTPL\_FUNCE\_SERIAL\_CAP\_FAX 0x06
#define CISTPL\_FUNCE\_SERIAL\_CAP\_VOICE 0x07
#define CISTPL\_FUNCE\_SERIAL\_IF\_DATA 0x08
#define CISTPL\_FUNCE\_SERIAL\_IF\_FAX 0x09
#define CISTPL\_FUNCE\_SERIAL\_IF\_VOICE 0x0a

/\* UART identification \*/ #define CISTPL\_SERIAL\_UART\_8250 0x00 #define CISTPL\_SERIAL\_UART\_16450 0x01 #define CISTPL\_SERIAL\_UART\_16550 0x02 #define CISTPL\_SERIAL\_UART\_8251 0x03 #define CISTPL\_SERIAL\_UART\_8530 0x04

#define CISTPL\_SERIAL\_UART\_85230 0x05

/\* UART capabilities \*/

#define CISTPL\_SERIAL\_UART\_SPACE 0x01
#define CISTPL\_SERIAL\_UART\_MARK 0x02
#define CISTPL\_SERIAL\_UART\_ODD 0x04
#define CISTPL\_SERIAL\_UART\_EVEN 0x08
#define CISTPL\_SERIAL\_UART\_6BIT 0x01
#define CISTPL\_SERIAL\_UART\_7BIT 0x04
#define CISTPL\_SERIAL\_UART\_8BIT 0x08
#define CISTPL\_SERIAL\_UART\_1STOP 0x10
#define CISTPL\_SERIAL\_UART\_MSTOP 0x20
#define CISTPL\_SERIAL\_UART\_2STOP 0x40

typedef struct cistpl\_serial\_t {
 u\_char uart\_type;
 u\_char uart\_cap\_0;
 u\_char uart\_cap\_1;
} cistpl\_serial\_t;

typedef struct cistpl\_modem\_cap\_t {
 u\_char flow;
 u\_char cmd\_buf;
 u\_char rcv\_buf\_0, rcv\_buf\_1, rcv\_buf\_2;
 u\_char xmit\_buf\_0, xmit\_buf\_1, xmit\_buf\_2;
} cistpl\_modem\_cap\_t;

#define CISTPL\_SERIAL\_MOD\_103 0x01
#define CISTPL\_SERIAL\_MOD\_V21 0x02
#define CISTPL\_SERIAL\_MOD\_V23 0x04
#define CISTPL\_SERIAL\_MOD\_V22 0x08
#define CISTPL\_SERIAL\_MOD\_V22BIS 0x20
#define CISTPL\_SERIAL\_MOD\_V26 0x40
#define CISTPL\_SERIAL\_MOD\_V26BIS 0x80
#define CISTPL\_SERIAL\_MOD\_V27BIS 0x01

```
#define CISTPL_SERIAL_MOD_V29 0x02
#define CISTPL SERIAL MOD V32 0x04
#define CISTPL SERIAL MOD V32BIS 0x08
#define CISTPL SERIAL MOD V34 0x10
#define CISTPL SERIAL ERR MNP2 4 0x01
#define CISTPL_SERIAL_ERR_V42_LAPM 0x02
#define CISTPL SERIAL CMPR V42BIS 0x01
#define CISTPL_SERIAL_CMPR_MNP5 0x02
#define CISTPL SERIAL CMD AT1 0x01
#define CISTPL SERIAL CMD AT2 0x02
#define CISTPL SERIAL CMD AT3 0x04
#define CISTPL SERIAL CMD MNP AT 0x08
#define CISTPL SERIAL CMD V25BIS 0x10
#define CISTPL_SERIAL_CMD_V25A 0x20
#define CISTPL SERIAL CMD DMCL 0x40
typedef struct cistpl data serv t {
u_char max_data_0;
u_char max_data_1;
u char modulation 0;
u_char modulation_1;
u char error control;
u_char compression;
u char cmd protocol;
u char escape;
u_char encrypt;
u_char misc_features;
u_char ccitt_code[1];
} cistpl_data_serv_t;
typedef struct cistpl_fax_serv_t {
u_char max_data_0;
u_char max_data_1;
u char modulation;
u_char encrypt;
u_char features_0;
u_char features_1;
u_char ccitt_code[1];
} cistpl_fax_serv_t;
typedef struct cistpl voice serv t {
u_char max_data_0;
u_char max_data_1;
```

/\*\_\_\_\_\_\_

} cistpl\_voice\_serv\_t;

### LAN Function Extension Tuples

\*/

#define CISTPL\_FUNCE\_LAN\_TECH 0x01
#define CISTPL\_FUNCE\_LAN\_SPEED 0x02
#define CISTPL\_FUNCE\_LAN\_MEDIA 0x03
#define CISTPL\_FUNCE\_LAN\_NODE\_ID 0x04
#define CISTPL\_FUNCE\_LAN\_CONNECTOR 0x05

/\* LAN technologies \*/
#define CISTPL\_LAN\_TECH\_ARCNET 0x01
#define CISTPL\_LAN\_TECH\_ETHERNET 0x02
#define CISTPL\_LAN\_TECH\_TOKENRING 0x03
#define CISTPL\_LAN\_TECH\_LOCALTALK 0x04
#define CISTPL\_LAN\_TECH\_FDDI 0x05
#define CISTPL\_LAN\_TECH\_ATM 0x06
#define CISTPL\_LAN\_TECH\_WIRELESS 0x07

typedef struct cistpl\_lan\_tech\_t {
u\_char tech;
} cistpl\_lan\_tech\_t;

typedef struct cistpl\_lan\_speed\_t {
 u\_int speed;
} cistpl\_lan\_speed\_t;

/\* LAN media definitions \*/
#define CISTPL\_LAN\_MEDIA\_UTP 0x01
#define CISTPL\_LAN\_MEDIA\_STP 0x02
#define CISTPL\_LAN\_MEDIA\_THIN\_COAX 0x03
#define CISTPL\_LAN\_MEDIA\_THICK\_COAX 0x04
#define CISTPL\_LAN\_MEDIA\_SUBAL\_SUB

typedef struct cistpl\_lan\_media\_t {
u\_char media;
} cistpl\_lan\_media\_t;

typedef struct cistpl\_lan\_node\_id\_t {
u\_char nb;
u\_char id[16];
} cistpl\_lan\_node\_id\_t;

typedef struct cistpl\_lan\_connector\_t {
 u\_char code;

} cistpl_lan_connector_t;
/*
IDE Function Extension Tuples
*/
#define CISTPL_IDE_INTERFACE 0x01
typedef struct cistpl_ide_interface_t {
u_char interface; } cistpl_ide_interface_t;
/* First feature byte */
#define CISTPL_IDE_SILICON 0x04
#define CISTPL_IDE_UNIQUE 0x08
#define CISTPL_IDE_DUAL 0x10
/* Second feature byte */
#define CISTPL_IDE_HAS_SLEEP 0x01
#define CISTPL_IDE_HAS_STANDBY 0x02
#define CISTPL_IDE_HAS_IDLE 0x04
#define CISTPL_IDE_LOW_POWER 0x08
#define CISTPL_IDE_REG_INHIBIT 0x10
#define CISTPL_IDE_HAS_INDEX 0x20
#define CISTPL_IDE_IOIS16 0x40
typedef struct cistpl_ide_feature_t {
u_char feature1;
u_char feature2;
} cistpl_ide_feature_t;
#define CISTPL_FUNCE_IDE_IFACE 0x01
#define CISTPL_FUNCE_IDE_MASTER 0x02
#define CISTPL_FUNCE_IDE_SLAVE 0x03
/*=====================================
Configuration Table Entries
*/
#define CISTPL_BAR_SPACE 0x07
#define CISTPL_BAR_SPACE_IO 0x10
#define CISTPL_BAR_PREFETCH 0x20
#define CISTPL_BAR_CACHEABLE 0x40
#define CISTPL BAR 1MEG MAP 0x80

typedef struct cistpl\_bar\_t {

u char attr; u int size; } cistpl bar t; typedef struct cistpl\_config\_t { u char last idx; u int base; u int rmask[4]; u char subtuples; } cistpl\_config\_t; /\* These are bits in the 'present' field, and indices in 'param' \*/ #define CISTPL POWER VNOM 0 #define CISTPL\_POWER\_VMIN 1 #define CISTPL POWER VMAX 2 #define CISTPL POWER ISTATIC 3 #define CISTPL\_POWER\_IAVG 4 #define CISTPL POWER IPEAK 5 #define CISTPL\_POWER\_IDOWN 6 #define CISTPL\_POWER\_HIGHZ\_OK 0x01 #define CISTPL\_POWER\_HIGHZ\_REQ 0x02 typedef struct cistpl\_power\_t { u char present; u\_char flags; u int param[7]; } cistpl\_power\_t; typedef struct cistpl\_timing\_t { u\_int wait, waitscale; u int ready, rdyscale; u\_int reserved, rsvscale; } cistpl\_timing\_t; #define CISTPL\_IO\_LINES\_MASK 0x1f #define CISTPL\_IO\_8BIT 0x20 #define CISTPL\_IO\_16BIT 0x40 #define CISTPL\_IO\_RANGE 0x80 #define CISTPL\_IO\_MAX\_WIN 16 typedef struct cistpl\_io\_t { u\_char flags; u\_char nwin; struct { u\_int base; u\_int len; } win[CISTPL\_IO\_MAX\_WIN]; } cistpl\_io\_t;

typedef struct cistpl\_irq\_t {
 u\_int IRQInfo1;
 u\_int IRQInfo2;
} cistpl\_irq\_t;

#define CISTPL\_MEM\_MAX\_WIN 8

typedef struct cistpl\_mem\_t {
 u\_char flags;
 u\_char nwin;
 struct {
 u\_int len;
 u\_int card\_addr;
 u\_int host\_addr;
 } win[CISTPL\_MEM\_MAX\_WIN];
 } cistpl\_mem\_t;

#define CISTPL\_CFTABLE\_DEFAULT 0x0001
#define CISTPL\_CFTABLE\_BVDS 0x0002
#define CISTPL\_CFTABLE\_WP 0x0004
#define CISTPL\_CFTABLE\_RDYBSY 0x0008
#define CISTPL\_CFTABLE\_MWAIT 0x0010
#define CISTPL\_CFTABLE\_AUDIO 0x0800
#define CISTPL\_CFTABLE\_READONLY 0x1000
#define CISTPL\_CFTABLE\_PWRDOWN 0x2000

typedef struct cistpl\_cftable\_entry\_t {
 u\_char index;
 u\_short flags;
 u\_char interface;
 cistpl\_power\_t vcc, vpp1, vpp2;
 cistpl\_timing\_t timing;
 cistpl\_io\_t io;
 cistpl\_irq\_t irq;
 cistpl\_mem\_t mem;
 u\_char subtuples;
 } cistpl\_cftable\_entry\_t;

#define CISTPL\_CFTABLE\_MASTER 0x000100
#define CISTPL\_CFTABLE\_INVALIDATE 0x000200
#define CISTPL\_CFTABLE\_VGA\_PALETTE 0x000400
#define CISTPL\_CFTABLE\_PARITY 0x000800
#define CISTPL\_CFTABLE\_WAIT 0x001000
#define CISTPL\_CFTABLE\_SERR 0x002000
#define CISTPL\_CFTABLE\_FAST\_BACK 0x004000
#define CISTPL\_CFTABLE\_BINARY\_AUDIO 0x010000
#define CISTPL\_CFTABLE\_PWM\_AUDIO 0x020000

typedef struct cistpl\_cftable\_entry\_cb\_t {

u char index; u int flags; cistpl\_power\_t vcc, vpp1, vpp2; u char io; cistpl\_irq\_t irq; u char mem; u\_char subtuples; } cistpl\_cftable\_entry\_cb\_t; typedef struct cistpl\_device\_geo\_t { u char ngeo; struct { u char buswidth; u\_int erase\_block; u int read block; u\_int write\_block; u\_int partition; u int interleave; } geo[CISTPL\_MAX\_DEVICES]; } cistpl\_device\_geo\_t; typedef struct cistpl\_vers\_2\_t { u\_char vers; u\_char comply; u short dindex; u\_char vspec8, vspec9; u char nhdr; u\_char vendor, info; char str[244]; } cistpl\_vers\_2\_t; typedef struct cistpl\_org\_t {

u\_char data\_org; char desc[30]; } cistpl\_org\_t;

#define CISTPL\_ORG\_FS 0x00
#define CISTPL\_ORG\_APPSPEC 0x01
#define CISTPL\_ORG\_XIP 0x02

typedef struct cistpl\_format\_t {
 u\_char type;
 u\_char edc;
 u\_int offset;
 u\_int length;
} cistpl\_format\_t;

#define CISTPL\_FORMAT\_DISK 0x00 #define CISTPL\_FORMAT\_MEM 0x01

#define CISTPL EDC NONE 0x00 #define CISTPL EDC CKSUM 0x01 #define CISTPL EDC CRC 0x02 #define CISTPL EDC PCC 0x03 typedef union cisparse t { cistpl device t device; cistpl checksum t checksum; cistpl longlink t longlink; cistpl\_longlink\_mfc\_t longlink\_mfc; cistpl vers 1 t version 1; cistpl\_altstr\_t altstr; cistpl jedec t jedec; cistpl manfid t manfid; cistpl funcid t funcid; cistpl funce t funce; cistpl\_bar\_t bar; cistpl config t config; cistpl\_cftable\_entry\_t cftable\_entry; cistpl cftable entry cb t cftable entry cb; cistpl\_device\_geo\_t device\_geo; cistpl\_vers\_2\_t vers\_2; cistpl\_org\_t org; cistpl\_format\_t format; } cisparse t; typedef struct tuple t { u int Attributes;

```
cisdata_t DesiredTuple;
u_int Flags; /* internal use */
u_int LinkOffset; /* internal use */
u_int CISOffset; /* internal use */
cisdata_t TupleCode;
cisdata_t TupleLink;
cisdata_t TupleOffset;
cisdata_t TupleDataMax;
cisdata_t TupleDataLen;
cisdata_t *TupleData;
} tuple_t;
```

/\* Special cisdata\_t value \*/
#define RETURN\_FIRST\_TUPLE 0xff

/\* Attributes for tuple calls \*/ #define TUPLE\_RETURN\_LINK 0x01 #define TUPLE\_RETURN\_COMMON 0x02

/\* For ValidateCIS \*/ typedef struct cisinfo\_t { u\_int Chains; } cisinfo\_t;

#define CISTPL\_MAX\_CIS\_SIZE 0x200

/\* For ReplaceCIS \*/
typedef struct cisdump\_t {
 u\_int Length;
 cisdata\_t Data[CISTPL\_MAX\_CIS\_SIZE];
} cisdump\_t;

#endif /\* CISTPL H \*/

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component: PIE version: 20120512 url: http://github.com/lojjic/PIE/ type: Apache-2.0

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component: quick-and-dirty XML parser url: http://www.javaworld.com/javatips/jw-javatip128.html type: MIT

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component: Quickfunctor url: http://sourceforge.net/projects/quickfunctor/ type: MIT

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component: Raphael JavaScript Library url: http://raphaeljs.com/ type: MIT

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component: remdebug url: http://github.com/LuaDist/remdebug/ type: MIT

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component: RFC 6234 Secure Hash Algorithm url: http://tools.ietf.org/html/rfc6234

#### type: RFC

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component: RSA Data Security-MD5 Message url: http://tools.ietf.org/html/rfc1321 type: RSA Data Security

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component: Struct library for LUA url: http://www.inf.puc-rio.br/~roberto/struct/ type: MIT

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component: timeentry url: http://github.com/kbwood/timeentry/ type: MIT

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component: Tiny Scrollbar jQuery plugin version: 1.81 url: http://baijs.nl/tinyscrollbar/ type: MIT

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component: Tooltip jQuery plugin version: 1.3 url: http://bassistance.de/jquery-plugins/jquery-plugin-tooltip/ type: MIT

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component: WinPcap - The Windows Packet Capture Library version: 4.0 url: http://www.winpcap.org/default.htm type: BSD-3-Clause

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component: XML Extras url: http://webfx.eae.net/dhtml/xmlextras/xmlextras.html type: Apache-2.0

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component: YUI Library version: 2.5.0 url: http://sourceforge.net/projects/yui/ type: BSD-3-Clause

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component: zlib url: http://www.zlib.net/ type: zlib

/\* zlib.h -- interface of the 'zlib' general purpose compression library version 1.2.8, April 28th, 2013

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Modified by Marcel Wijkstra <wijkstra@fwi.uva.nl>

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hash.c:

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\* Author: breese@users.sourceforge.net

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list.c:

/\*

\* list.c: lists handling implementation

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component: RFC 6234 Secure Hash Algorithm url: http://tools.ietf.org/html/rfc6234 type: RFC

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/\* zlib.h -- interface of the 'zlib' general purpose compression library version 1.2.8, April 28th, 2013

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component: Intel Media SDK for Clients version: 2014 (5.0.0000337.78585) url: http://software.intel.com/en-us/vcsource/tools/media-sdk type: BSD-3-Clause

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component: jQuery JavaScript Library version: 1.7.1 / 1.8.3 / 1.10.2 url: http://jquery.com/ type: MIT

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component: jQuery UI from code.google.com version: 1.8 url: http://code.google.com/p/jquery-ui/ type: MIT

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component: jquery-handsontable url: http://github.com/warpech/jquery-handsontable/ type: MIT

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component: jquery-mobile version: 1.3.2 url: http://github.com/jquery/jquery-mobile/ type: MIT

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component: jquery-treeview

url: http://github.com/jzaefferer/jquery-treeview/ type: MIT

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component: jquery-ui-touch-punch url: http://github.com/furf/jquery-ui-touch-punch/ type: MIT

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component: jquery.nicescroll url: http://github.com/inuyaksa/jquery.nicescroll/ type: MIT

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component: jQuery.selectBoxlt.js url: http://github.com/gfranko/jQuery.selectBoxlt.js/ type: MIT

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component: jqueryjs version: 1.3.2 url: http://code.google.com/p/jqueryjs/ type: MIT

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component: jqueryui.com version: 1.10.0-2 url: http://github.com/jquery/jqueryui.com/ type: MIT

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component: jScrollPane version: 2.0.14 and other url: http://github.com/vitch/jScrollPane/ type: MIT

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component: jStorage version: 0.4.0 url: https://github.com/andris9/jStorage/ type: MIT

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component: jsTree url: http://sourceforge.net/projects/jstree/ type: MIT

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component: knockout-deferred-updates url: https://github.com/mbest/knockout-deferred-updates/ type: MIT

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component: Lazy Load Plugin for jQuery url: http://www.appelsiini.net/projects/lazyload type: MIT Copyright (c) 2007-2013 Mika Tuupola

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component: libusb-win32 version: 1.2.6.0 url: https://sourceforge.net/projects/libusb-win32/ type: LGPLv3

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component: lua

url: http://www.lua.org/ type: MIT

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component: luasocket url: http://github.com/fengye/luasocket/ type: MIT

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component: Paul Johnson's JavaScript Message Digest Hash Function Library version: 2.1 url: http://pajhome.org.uk/crypt/md5/ type: BSD-3-Clause

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component: pbmplus url: http://acme.com/software/pbmplus/ type: MIT

Code drawn from ppmtogif.c, from the pbmplus package

Based on GIFENCOD by David Rowley <mgardi@watdscu.waterloo.edu>. A Lempel-Zim compression based on "compress".

Modified by Marcel Wijkstra <wijkstra@fwi.uva.nl>

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Heavily modified by Mouse, 1998-02-12. Remove LZW compression. Added miGIF run length compression.

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component: pcmcia-cs version: 3.2.8 url: http://github.com/lumag/pcmcia-cs/ type: MPL 1.1

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/\* \* cistpl.h 1.35 2001/08/24 12:16:12

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# /\*

- \* Changes compared to the original from pcmcia-cs, version 3.2.8:
- \* 2014-02-23, Joachim Meichle:
- \* Rename inclusion guard from "\_LINUX\_CISTPL\_H" to "CISTPL\_H"
- \* Replace zero size arrays with arrays of size one.
- \* Zero sized arrays are an non-standard and a compiler specific
- \* extension of GCC.
- \* Add "typedef u\_char cisdata\_t;" to make file "more" self-containing \*/

#ifndef CISTPL\_H #define CISTPL\_H

```
#define CISTPL_NULL 0x00
#define CISTPL_DEVICE 0x01
#define CISTPL_LONGLINK_CB 0x02
#define CISTPL_INDIRECT 0x03
#define CISTPL_CONFIG_CB 0x04
#define CISTPL_CFTABLE_ENTRY_CB 0x05
#define CISTPL_LONGLINK_MFC 0x06
#define CISTPL_BAR 0x07
#define CISTPL_PWR_MGMNT 0x08
#define CISTPL_EXTDEVICE 0x09
#define CISTPL_CHECKSUM 0x10
#define CISTPL_LONGLINK_A 0x11
#define CISTPL_LONGLINK C 0x12
```

#define CISTPL LINKTARGET 0x13 #define CISTPL NO LINK 0x14 #define CISTPL VERS 1 0x15 #define CISTPL ALTSTR 0x16 #define CISTPL DEVICE A 0x17 #define CISTPL JEDEC C 0x18 #define CISTPL\_JEDEC\_A 0x19 #define CISTPL CONFIG 0x1a #define CISTPL CFTABLE ENTRY 0x1b #define CISTPL\_DEVICE\_OC 0x1c #define CISTPL DEVICE OA 0x1d #define CISTPL\_DEVICE\_GEO 0x1e #define CISTPL DEVICE GEO A 0x1f #define CISTPL\_MANFID 0x20 #define CISTPL FUNCID 0x21 #define CISTPL FUNCE 0x22 #define CISTPL\_SWIL 0x23 #define CISTPL END 0xff /\* Layer 2 tuples \*/ #define CISTPL VERS 2 0x40 #define CISTPL FORMAT 0x41 #define CISTPL\_GEOMETRY 0x42 #define CISTPL\_BYTEORDER 0x43 #define CISTPL\_DATE 0x44 #define CISTPL BATTERY 0x45 #define CISTPL FORMAT A 0x47 /\* Layer 3 tuples \*/ #define CISTPL ORG 0x46 #define CISTPL\_SPCL 0x90

typedef u\_char cisdata\_t;

typedef struct cistpl\_longlink\_t {
 u\_int addr;
} cistpl\_longlink\_t;

typedef struct cistpl\_checksum\_t {
 u\_short addr;
 u\_short len;
 u\_char sum;
} cistpl\_checksum\_t;

#define CISTPL\_MAX\_FUNCTIONS 8
#define CISTPL\_MFC\_ATTR 0x00
#define CISTPL\_MFC\_COMMON 0x01

typedef struct cistpl\_longlink\_mfc\_t {
u\_char nfn;
struct {
u\_char space;

u int addr; } fn[CISTPL MAX FUNCTIONS]; } cistpl\_longlink\_mfc\_t; #define CISTPL\_MAX\_ALTSTR\_STRINGS 4 typedef struct cistpl\_altstr\_t { u char ns; u char ofs[CISTPL MAX ALTSTR STRINGS]; char str[254]; } cistpl\_altstr\_t; #define CISTPL DTYPE NULL 0x00 #define CISTPL DTYPE ROM 0x01 #define CISTPL DTYPE OTPROM 0x02 #define CISTPL DTYPE EPROM 0x03 #define CISTPL\_DTYPE\_EEPROM 0x04 #define CISTPL DTYPE FLASH 0x05 #define CISTPL DTYPE SRAM 0x06 #define CISTPL DTYPE DRAM 0x07 #define CISTPL DTYPE FUNCSPEC 0x0d #define CISTPL\_DTYPE\_EXTEND 0x0e #define CISTPL\_MAX\_DEVICES 4 typedef struct cistpl\_device\_t { u char ndev; struct { u\_char type; u\_char wp; u\_int speed; u int size; } dev[CISTPL\_MAX\_DEVICES]; } cistpl\_device\_t; #define CISTPL\_DEVICE\_MWAIT 0x01 #define CISTPL\_DEVICE\_3VCC 0x02 typedef struct cistpl\_device\_o\_t { u\_char flags; cistpl\_device\_t device;

} cistpl\_device\_o\_t;

#define CISTPL\_VERS\_1\_MAX\_PROD\_STRINGS 4

typedef struct cistpl\_vers\_1\_t {
 u\_char major;
 u\_char minor;
 u\_char ns;
 u\_char ofs[CISTPL\_VERS\_1\_MAX\_PROD\_STRINGS];

char str[254]; } cistpl\_vers\_1\_t; typedef struct cistpl\_jedec\_t { u char nid; struct { u char mfr; u char info; } id[CISTPL MAX DEVICES]; } cistpl\_jedec\_t; typedef struct cistpl\_manfid\_t { u short manf; u short card; } cistpl manfid t; #define CISTPL\_FUNCID\_MULTI 0x00 #define CISTPL FUNCID MEMORY 0x01 #define CISTPL\_FUNCID\_SERIAL 0x02 #define CISTPL FUNCID PARALLEL 0x03 #define CISTPL\_FUNCID\_FIXED 0x04 #define CISTPL\_FUNCID\_VIDEO 0x05 #define CISTPL\_FUNCID\_NETWORK 0x06 #define CISTPL\_FUNCID\_AIMS 0x07 #define CISTPL\_FUNCID\_SCSI 0x08 #define CISTPL SYSINIT POST 0x01 #define CISTPL\_SYSINIT\_ROM 0x02 typedef struct cistpl\_funcid\_t { u\_char func; u char sysinit; } cistpl\_funcid\_t; typedef struct cistpl\_funce\_t { u\_char type; u\_char data[1]; } cistpl\_funce\_t; Modem Function Extension Tuples -----\*/ #define CISTPL\_FUNCE\_SERIAL\_IF 0x00 #define CISTPL\_FUNCE\_SERIAL\_CAP 0x01 #define CISTPL\_FUNCE\_SERIAL\_SERV\_DATA 0x02 #define CISTPL\_FUNCE\_SERIAL\_SERV\_FAX 0x03 #define CISTPL\_FUNCE\_SERIAL\_SERV\_VOICE 0x04

#define CISTPL\_FUNCE\_SERIAL\_CAP\_DATA 0x05
#define CISTPL\_FUNCE\_SERIAL\_CAP\_FAX 0x06
#define CISTPL\_FUNCE\_SERIAL\_CAP\_VOICE 0x07
#define CISTPL\_FUNCE\_SERIAL\_IF\_DATA 0x08
#define CISTPL\_FUNCE\_SERIAL\_IF\_FAX 0x09
#define CISTPL\_FUNCE\_SERIAL\_IF\_VOICE 0x0a

/\* UART identification \*/
#define CISTPL\_SERIAL\_UART\_8250 0x00
#define CISTPL\_SERIAL\_UART\_16450 0x01
#define CISTPL\_SERIAL\_UART\_16550 0x02
#define CISTPL\_SERIAL\_UART\_8251 0x03
#define CISTPL\_SERIAL\_UART\_8530 0x04
#define CISTPL\_SERIAL\_UART\_85230 0x05

/\* UART capabilities \*/

#define CISTPL\_SERIAL\_UART\_SPACE 0x01 #define CISTPL\_SERIAL\_UART\_MARK 0x02 #define CISTPL\_SERIAL\_UART\_ODD 0x04 #define CISTPL\_SERIAL\_UART\_EVEN 0x08 #define CISTPL\_SERIAL\_UART\_5BIT 0x01 #define CISTPL\_SERIAL\_UART\_6BIT 0x02 #define CISTPL\_SERIAL\_UART\_7BIT 0x04 #define CISTPL\_SERIAL\_UART\_8BIT 0x08 #define CISTPL\_SERIAL\_UART\_1STOP 0x10 #define CISTPL\_SERIAL\_UART\_1STOP 0x20 #define CISTPL\_SERIAL\_UART\_2STOP 0x40

typedef struct cistpl\_serial\_t {
 u\_char uart\_type;
 u\_char uart\_cap\_0;
 u\_char uart\_cap\_1;
 } cistpl\_serial\_t;

typedef struct cistpl\_modem\_cap\_t {
 u\_char flow;
 u\_char cmd\_buf;
 u\_char rcv\_buf\_0, rcv\_buf\_1, rcv\_buf\_2;
 u\_char xmit\_buf\_0, xmit\_buf\_1, xmit\_buf\_2;
} cistpl\_modem\_cap\_t;

#define CISTPL\_SERIAL\_MOD\_103 0x01
#define CISTPL\_SERIAL\_MOD\_V21 0x02
#define CISTPL\_SERIAL\_MOD\_V23 0x04
#define CISTPL\_SERIAL\_MOD\_V22 0x08
#define CISTPL\_SERIAL\_MOD\_V22BIS 0x20
#define CISTPL\_SERIAL\_MOD\_V26 0x40
#define CISTPL\_SERIAL\_MOD\_V26BIS 0x80
#define CISTPL\_SERIAL\_MOD\_V27BIS 0x01

#define CISTPL\_SERIAL\_MOD\_V29 0x02
#define CISTPL\_SERIAL\_MOD\_V32 0x04
#define CISTPL\_SERIAL\_MOD\_V32BIS 0x08
#define CISTPL\_SERIAL\_MOD\_V34 0x10

#define CISTPL\_SERIAL\_ERR\_MNP2\_4 0x01 #define CISTPL\_SERIAL\_ERR\_V42\_LAPM 0x02

#define CISTPL\_SERIAL\_CMPR\_V42BIS 0x01
#define CISTPL\_SERIAL\_CMPR\_MNP5 0x02

#define CISTPL\_SERIAL\_CMD\_AT1 0x01
#define CISTPL\_SERIAL\_CMD\_AT2 0x02
#define CISTPL\_SERIAL\_CMD\_AT3 0x04
#define CISTPL\_SERIAL\_CMD\_MNP\_AT 0x08
#define CISTPL\_SERIAL\_CMD\_V25BIS 0x10
#define CISTPL\_SERIAL\_CMD\_V25A 0x20
#define CISTPL\_SERIAL\_CMD\_DMCL 0x40

typedef struct cistpl\_data\_serv\_t {
 u\_char max\_data\_0;
 u\_char max\_data\_1;
 u\_char modulation\_0;
 u\_char modulation\_1;
 u\_char error\_control;
 u\_char compression;
 u\_char cmd\_protocol;
 u\_char encrypt;
 u\_char misc\_features;
 u\_char ccitt\_code[1];
 } cistpl\_data\_serv\_t;

typedef struct cistpl\_fax\_serv\_t {
 u\_char max\_data\_0;
 u\_char max\_data\_1;
 u\_char modulation;
 u\_char encrypt;
 u\_char features\_0;
 u\_char features\_1;
 u\_char ccitt\_code[1];
 } cistpl\_fax\_serv\_t;

typedef struct cistpl\_voice\_serv\_t {
 u\_char max\_data\_0;
 u\_char max\_data\_1;
} cistpl\_voice\_serv\_t;

/\*\_\_\_\_\_

#### LAN Function Extension Tuples

\*/

#define CISTPL\_FUNCE\_LAN\_TECH 0x01
#define CISTPL\_FUNCE\_LAN\_SPEED 0x02
#define CISTPL\_FUNCE\_LAN\_MEDIA 0x03
#define CISTPL\_FUNCE\_LAN\_NODE\_ID 0x04
#define CISTPL\_FUNCE\_LAN\_CONNECTOR 0x05

/\* LAN technologies \*/
#define CISTPL\_LAN\_TECH\_ARCNET 0x01
#define CISTPL\_LAN\_TECH\_ETHERNET 0x02
#define CISTPL\_LAN\_TECH\_TOKENRING 0x03
#define CISTPL\_LAN\_TECH\_LOCALTALK 0x04
#define CISTPL\_LAN\_TECH\_FDDI 0x05
#define CISTPL\_LAN\_TECH\_ATM 0x06
#define CISTPL\_LAN\_TECH\_WIRELESS 0x07

typedef struct cistpl\_lan\_tech\_t {
 u\_char tech;
} cistpl\_lan\_tech\_t;

typedef struct cistpl\_lan\_speed\_t {
 u\_int speed;
} cistpl\_lan\_speed\_t;

/\* LAN media definitions \*/
#define CISTPL\_LAN\_MEDIA\_UTP 0x01
#define CISTPL\_LAN\_MEDIA\_STP 0x02
#define CISTPL\_LAN\_MEDIA\_THIN\_COAX 0x03
#define CISTPL\_LAN\_MEDIA\_THICK\_COAX 0x04
#define CISTPL\_LAN\_MEDIA\_SIBER 0x05
#define CISTPL\_LAN\_MEDIA\_2GHZ 0x06
#define CISTPL\_LAN\_MEDIA\_5GHZ 0x08
#define CISTPL\_LAN\_MEDIA\_DIFF\_IR 0x09
#define CISTPL\_LAN\_MEDIA\_PTP\_IR 0x0a

typedef struct cistpl\_lan\_media\_t {
 u\_char media;
} cistpl\_lan\_media\_t;

typedef struct cistpl\_lan\_node\_id\_t {
u\_char nb;
u\_char id[16];
} cistpl\_lan\_node\_id\_t;

typedef struct cistpl\_lan\_connector\_t {
u\_char code;

} cistpl_lan_connector_t;
/*=====================================
IDE Function Extension Tuples
*/
#define CISTPL_IDE_INTERFACE 0x01
typedef struct cistpl_ide_interface_t { u_char interface; } cistpl_ide_interface_t;
/* First feature byte */ #define CISTPL_IDE_SILICON 0x04 #define CISTPL_IDE_UNIQUE 0x08 #define CISTPL_IDE_DUAL 0x10
/* Second feature byte */ #define CISTPL_IDE_HAS_SLEEP 0x01 #define CISTPL_IDE_HAS_STANDBY 0x02 #define CISTPL_IDE_HAS_IDLE 0x04 #define CISTPL_IDE_LOW_POWER 0x08 #define CISTPL_IDE_REG_INHIBIT 0x10 #define CISTPL_IDE_HAS_INDEX 0x20 #define CISTPL_IDE_IOIS16 0x40
<pre>typedef struct cistpl_ide_feature_t {   u_char feature1;   u_char feature2; } cistpl_ide_feature_t;</pre>
#define CISTPL_FUNCE_IDE_IFACE 0x01 #define CISTPL_FUNCE_IDE_MASTER 0x02 #define CISTPL_FUNCE_IDE_SLAVE 0x03
/*=====================================
Configuration Table Entries
*/
<pre>#define CISTPL_BAR_SPACE 0x07 #define CISTPL_BAR_SPACE_IO 0x10 #define CISTPL_BAR_PREFETCH 0x20 #define CISTPL_BAR_CACHEABLE 0x40 #define CISTPL_BAR_1MEG_MAP 0x80</pre>

typedef struct cistpl\_bar\_t {

u char attr; u int size; } cistpl bar t; typedef struct cistpl\_config\_t { u char last idx; u int base; u int rmask[4]; u char subtuples; } cistpl\_config\_t; /\* These are bits in the 'present' field, and indices in 'param' \*/ #define CISTPL POWER VNOM 0 #define CISTPL\_POWER\_VMIN 1 #define CISTPL POWER VMAX 2 #define CISTPL POWER ISTATIC 3 #define CISTPL\_POWER\_IAVG 4 #define CISTPL POWER IPEAK 5 #define CISTPL\_POWER\_IDOWN 6 #define CISTPL\_POWER\_HIGHZ\_OK 0x01 #define CISTPL\_POWER\_HIGHZ\_REQ 0x02 typedef struct cistpl\_power\_t { u char present; u\_char flags; u int param[7]; } cistpl\_power\_t; typedef struct cistpl\_timing\_t { u\_int wait, waitscale; u int ready, rdyscale; u\_int reserved, rsvscale; } cistpl\_timing\_t; #define CISTPL\_IO\_LINES\_MASK 0x1f #define CISTPL IO 8BIT 0x20 #define CISTPL\_IO\_16BIT 0x40 #define CISTPL\_IO\_RANGE 0x80 #define CISTPL\_IO\_MAX\_WIN 16 typedef struct cistpl\_io\_t { u\_char flags; u\_char nwin; struct { u\_int base; u\_int len; } win[CISTPL\_IO\_MAX\_WIN]; } cistpl\_io\_t;

typedef struct cistpl\_irq\_t {
 u\_int IRQInfo1;
 u\_int IRQInfo2;
} cistpl\_irq\_t;

#define CISTPL\_MEM\_MAX\_WIN 8

typedef struct cistpl\_mem\_t {
 u\_char flags;
 u\_char nwin;
 struct {
 u\_int len;
 u\_int card\_addr;
 u\_int host\_addr;
 } win[CISTPL\_MEM\_MAX\_WIN];
 } cistpl\_mem\_t;

#define CISTPL\_CFTABLE\_DEFAULT 0x0001
#define CISTPL\_CFTABLE\_BVDS 0x0002
#define CISTPL\_CFTABLE\_WP 0x0004
#define CISTPL\_CFTABLE\_RDYBSY 0x0008
#define CISTPL\_CFTABLE\_MWAIT 0x0010
#define CISTPL\_CFTABLE\_AUDIO 0x0800
#define CISTPL\_CFTABLE\_READONLY 0x1000
#define CISTPL\_CFTABLE\_PWRDOWN 0x2000

typedef struct cistpl\_cftable\_entry\_t {
 u\_char index;
 u\_short flags;
 u\_char interface;
 cistpl\_power\_t vcc, vpp1, vpp2;
 cistpl\_timing\_t timing;
 cistpl\_io\_t io;
 cistpl\_irq\_t irq;
 cistpl\_mem\_t mem;
 u\_char subtuples;
} cistpl\_cftable\_entry\_t;

#define CISTPL\_CFTABLE\_MASTER 0x000100
#define CISTPL\_CFTABLE\_INVALIDATE 0x000200
#define CISTPL\_CFTABLE\_VGA\_PALETTE 0x000400
#define CISTPL\_CFTABLE\_PARITY 0x000800
#define CISTPL\_CFTABLE\_WAIT 0x001000
#define CISTPL\_CFTABLE\_SERR 0x002000
#define CISTPL\_CFTABLE\_FAST\_BACK 0x004000
#define CISTPL\_CFTABLE\_BINARY\_AUDIO 0x010000
#define CISTPL\_CFTABLE\_PWM\_AUDIO 0x020000

typedef struct cistpl\_cftable\_entry\_cb\_t {

u char index; u\_int flags; cistpl\_power\_t vcc, vpp1, vpp2; u chario; cistpl\_irq\_t irq; u char mem; u\_char subtuples; } cistpl\_cftable\_entry\_cb\_t; typedef struct cistpl\_device\_geo\_t { u char ngeo; struct { u char buswidth; u\_int erase\_block; u int read block; u\_int write\_block; u\_int partition; u int interleave; } geo[CISTPL\_MAX\_DEVICES]; } cistpl\_device\_geo\_t; typedef struct cistpl\_vers\_2\_t { u\_char vers; u\_char comply; u short dindex; u\_char vspec8, vspec9; u char nhdr; u\_char vendor, info; char str[244]; } cistpl\_vers\_2\_t; typedef struct cistpl\_org\_t { u\_char data\_org; char desc[30]; } cistpl\_org\_t; #define CISTPL\_ORG\_FS 0x00 #define CISTPL\_ORG\_APPSPEC 0x01 #define CISTPL\_ORG\_XIP 0x02 typedef struct cistpl\_format\_t { u\_char type; u\_char edc;

u\_int offset; u\_int length; } cistpl\_format\_t;

#define CISTPL\_FORMAT\_DISK 0x00 #define CISTPL\_FORMAT\_MEM 0x01

#define CISTPL EDC NONE 0x00 #define CISTPL EDC CKSUM 0x01 #define CISTPL EDC CRC 0x02 #define CISTPL EDC PCC 0x03 typedef union cisparse t { cistpl\_device\_t device; cistpl checksum t checksum; cistpl longlink t longlink; cistpl\_longlink\_mfc\_t longlink\_mfc; cistpl vers 1 t version 1; cistpl\_altstr\_t altstr; cistpl jedec t jedec; cistpl\_manfid\_t manfid; cistpl funcid t funcid; cistpl funce t funce; cistpl\_bar\_t bar; cistpl config t config; cistpl\_cftable\_entry\_t cftable\_entry; cistpl cftable entry cb t cftable entry cb; cistpl\_device\_geo\_t device\_geo; cistpl\_vers\_2\_t vers\_2; cistpl\_org\_t org; cistpl\_format\_t format; } cisparse t;

```
typedef struct tuple_t {
  u_int Attributes;
  cisdata_t DesiredTuple;
  u_int Flags; /* internal use */
  u_int LinkOffset; /* internal use */
  u_int CISOffset; /* internal use */
  cisdata_t TupleCode;
  cisdata_t TupleLink;
  cisdata_t TupleOffset;
  cisdata_t TupleDataMax;
  cisdata_t TupleDataLen;
  cisdata_t *TupleData;
  } tuple_t;
```

/\* Special cisdata\_t value \*/ #define RETURN\_FIRST\_TUPLE 0xff

/\* Attributes for tuple calls \*/ #define TUPLE\_RETURN\_LINK 0x01 #define TUPLE\_RETURN\_COMMON 0x02

/\* For ValidateCIS \*/ typedef struct cisinfo\_t { u\_int Chains; } cisinfo\_t;

#define CISTPL\_MAX\_CIS\_SIZE 0x200

/\* For ReplaceCIS \*/
typedef struct cisdump\_t {
 u\_int Length;
 cisdata\_t Data[CISTPL\_MAX\_CIS\_SIZE];
} cisdump\_t;

```
#endif /* CISTPL_H */
```

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component: PIE version: 20120512 url: http://github.com/lojjic/PIE/ type: Apache-2.0

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component: PolyK library url: http://polyk.ivank.net/ type: MIT

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component: quick-and-dirty XML parser url: http://www.javaworld.com/javatips/jw-javatip128.html type: MIT

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component: Raphael JavaScript Library url: http://raphaeljs.com/ type: MIT

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component: RapidXml version: 1.13 url: http://sourceforge.net/projects/rapidxml/ type: MIT

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component: remdebug url: http://github.com/LuaDist/remdebug/ type: MIT

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component: six11hacks url: http://code.google.com/p/six11hacks/ type: BSD-3-Clause

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component: Struct library for LUA url: http://www.inf.puc-rio.br/~roberto/struct/ type: MIT

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component: The Loki Library url: http://loki-lib.sourceforge.net/ type: MIT

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component: timeentry

url: http://github.com/kbwood/timeentry/ type: MIT

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component: Tiny Scrollbar jQuery plugin version: 1.81 url: http://baijs.nl/tinyscrollbar/ type: MIT

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component: Tooltip jQuery plugin version: 1.3 url: http://bassistance.de/jquery-plugins/jquery-plugin-tooltip/ type: MIT

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component: U-Boot version: 2013.01.01 url: ftp://ftp.denx.de/pub/u-boot/u-boot-2013.01.01.tar.bz2 type: GPLv2

The component was modified. The used source code can be downloaded here: http://www.boschsecurity.com/oss/ ("CPP6", "Preloader based on u-boot")

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U-Boot

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component: zlib version: 1.2.3 url: http://www.zlib.net/ type: zlib

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